

METHOD FOR IMPLEMENTING DYNAMIC VIRTUAL LANE BUFFER RECONFIGURATION

Abstract of the Disclosure

5 A method, apparatus and computer program product are provided for
implementing dynamic Virtual Lane buffer reconfiguration in a channel
adapter. A first register is provided for communicating an adapter buffer size
and allocation capability for the channel adapter. At least one second
register is provided for communicating a current port buffer size and one
10 second register is associated with each physical port of the channel adapter.
A plurality of third registers is provided for communicating a current VL buffer
size, and one third register is associated with each VL of each physical port
of the channel adapter. The second register is used for receiving change
requests for adjusting the current port buffer size for an associated physical
15 port. The third register is used for receiving change requests for adjusting
the current VL buffer size for an associated VL.